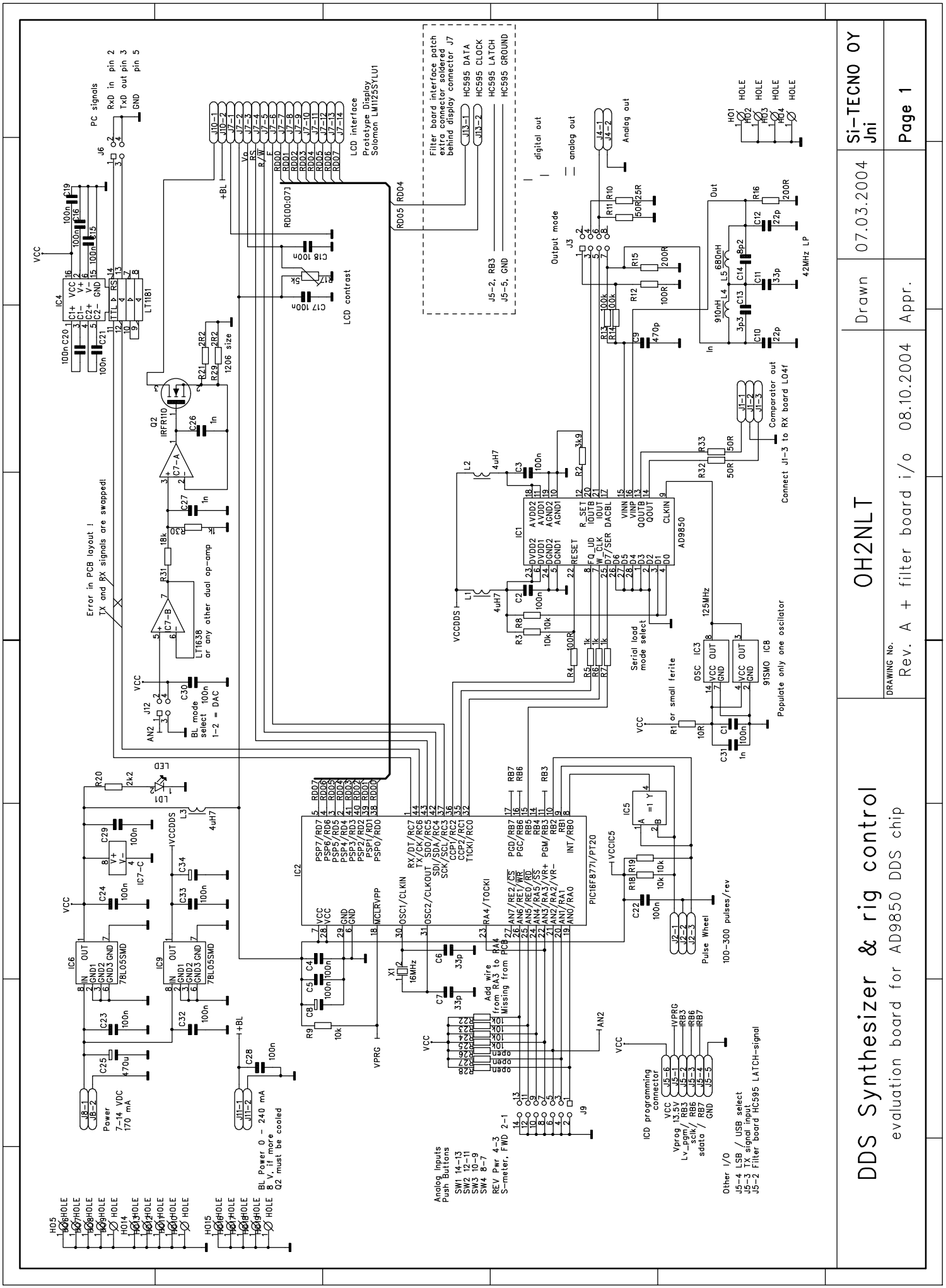
DDS Synthesizer & rig control
evaluation board for AD9850 DDS chip
OH2NLT
DRAWING No.
Rev. A + filter board i/o 08.10.2004
Drawn
07.03.2004
Appr.
Si-TECNO OY
Jni
Page 1
PC signals
RxD in pin 2
TxD out pin 3
GND pin 5
LCD interface
Prototype Display
Solomon LM1125SYLU1
Filter board interface patch
extra connector soldered
behind display connector J7
HC595 DATA
HC595 CLOCK
HC595 LATCH
HC595 GROUND
J5-2, RB3
J5-5, GND
digital out
analog out
Analog out
Output mode
LCD contrast
Error in PCB layout !
TX and RX signals are swapped!
LT1638
or any other dual op-amp
BL mode
select 100n
1-2 = DAC
Comparator out
Connect J1-3 to RX board L04f
Serial load
mode select
Populate only one oscilator
125MHz
91SMO ICB
42MHz LP
Pulse Wheel
100-300 pulses/rev
Add wire
from RA3 to RA4
Missing from PCB
Power
7-14 VDC
170 mA
BL Power 0 - 240 mA
8 V, if more
Q2 must be cooled
Analog Inputs
Push Buttons
SW1 14-13
SW2 12-11
SW3 10-9
SW4 8-7
REV Pwr 4-3
S-meter, FWD 2-1
ICD programming
connector
Other I/O
J5-4 LSB / USB select
J5-3 TX signal input
J5-2 Filter board HC595 LATCH-signal
AD9850
PIC16F877I/PT20
78L05SMD
LT1181
IRFR110
1206 size
HOLE